

**REMARKS IN RESPONSE**

Claims 1-6 and 8-19 are pending in the application.

Claims 7 and 20 have been deleted.

Claims 1-2, 5, 8-9, 11-12, 14-15, and 17-18 have been preliminarily amended, as set forth above.

**AMENDMENTS WITH MARKINGS TO SHOW CHANGES MADE****IN THE SPECIFICATION:**

1.

**CROSS-REFERENCE TO RELATED APPLICATIONS**

This application is a continuation application of prior application Ser. No. 09/127,050 filed July 31, 1998.

2.

Now referring to FIGURE 3B, there is shown a cross-sectional view cut along line [A-A] 3B-3B of FIGURE 3A. The conductors 120a, 120b, 120c are formed in a insulating layer 200 (of an integrated circuit or printed circuit board, or the like). Additional layers of substrate may be provided, such as a substrate layer 202. The conductors 120b and 120c are each spaced apart substantially laterally from the conductor 120a, with the conductor 120b positioned along one side of the conductor 120a and the conductor 120c positioned along the other side of the conductor 120a. As will be appreciated, using present processes and methods, the width of each of the conductors is generally about 0.7 microns and the spacing therebetween is about 0.7 microns. However, the width and spacing dimensions may vary, and elements/dimensions in the figure may vary and may not be drawn to scale. It is expected that next generation processes will generate widths on the order of 0.2 to 0.4 microns, and perhaps even smaller.

IN THE CLAIMS:

1. (Amended) An apparatus for decreasing the propagation delay time of an electrical signal transmitted from a source along a conductor in a circuit, the apparatus comprising:

a first conductor having a length extending from a first area of the circuit to a second area of the circuit and for carrying [the] an electrical signal, the first conductor having a first  
5 end electrically coupled to a source capable of providing the electrical signal; and

a second conductor located proximate the first conductor and extending substantially parallel and along the first conductor, the second conductor having a first end electrically  
coupled to the source [electrically coupled to the first conductor], and wherein the second  
conductor reduces the effective capacitance of the first conductor thereby increasing the  
10 speed of the electrical signal when transmitted along the first conductor.

2. (Amended) The apparatus in accordance with Claim 1 further comprising a third conductor located proximate the first conductor and extending substantially parallel and along the first conductor, the third conductor having a first end electrically coupled to the source [first conductor].

5. (Amended) The apparatus in accordance with Claim 3 further comprising a fourth conductor located proximate the first conductor and extending substantially parallel and along the first conductor, the fourth conductor electrically coupled to the source [first conductor].

8. (Amended) The apparatus in accordance with Claim [7] 1 wherein the first conductor and the second conductor each comprise metal.

9. (Amended) The apparatus in accordance with Claim 1 wherein the apparatus reduces the propagation delay of a clock signal when transmitted on the first conductor.

11. (Amended) An electrical conductor for increasing the speed of an electrical signal transmitted along the conductor in an integrated circuit, the conductor comprising:

a first conductor having a first end in a first area of the integrated circuit and a second end in a second area of the integrated circuit, and having a length extending from [a] the first area to the second area [of the integrated circuit to a second area of the integrated circuit];

a second conductor located proximate the first conductor and having a first end in the first area of the integrated circuit and a second end in a second area of the integrated circuit, and extending substantially parallel and along the first conductor from the first area to the second area; and

means for electrically coupling the first end of the first conductor to the first end of the second conductor, and wherein the second end of the first conductor and the second end of the second conductor are not electrically coupled in the second area of the integrated circuit.

12. (Amended) The electrical conductor in accordance with Claim 11 further comprising:

a third conductor located proximate the first conductor and having a first end in the first area of the integrated circuit and a second end in a second area of the integrated circuit,  
5 and extending substantially parallel and along the first conductor from the first area to the second area; and

means for electrically coupling the first end of the first conductor to the first end of the third conductor, and wherein the second end of the first conductor and the second end of the third conductor are not electrically connected in the second area of the integrated circuit.

14. (Amended) The electrical conductor in accordance with Claim 12 wherein the coupling of the first conductor to the second conductor and to the third conductor decreases the effective capacitance of the first conductor thus decreasing the propagation delay time of [the] an electrical signal when transmitted along the first conductor from the  
5 first area to the second area of the integrated circuit.

15. (Amended) The electrical conductor in accordance with Claim 11 wherein the electrical coupling of the first conductor to the second conductor decreases the effective capacitance of the first conductor thus decreasing the propagation delay time of [the] an electrical signal when transmitted along the first conductor from the first area to the second area of the integrated circuit.

17. (Amended) A conductor for transmitting a clocking signal from a first area to a second area of an integrated circuit, the conductor comprising:

a first elongated conductive portion extending from the first area to the second area;

5 a second elongated conductive portion located proximate and space apart from the first conductive portion and extending substantially parallel with the first conductive portion from the first area to the second area;

a third elongated conductive portion located proximate and space apart from the first conductive portion and extending substantially parallel with the first conductive portion from the first area to the second area;

10 means for electrically connecting the first conductive portion to the second conductive portion; [and]

means for electrically connecting the first conductive portion to the third conductive portion; and

15 a source located within the first area and coupled to the first, second and third conductive portions and capable of generating a clocking signal for transmission on the first conductive portion from the first area to the second area.

18. (Amended) The conductor in accordance with Claim 17 wherein the second conductive portion and the third conductive portion reduce the capacitive effects on the first conductive portion thereby reducing the propagation delay of the clocking signal when transmitted from the first area to the second area.

As a result of the foregoing, the Applicant asserts that the remaining Claims in the Application are in condition for allowance, and respectfully requests an early allowance of such Claims.

Applicant respectfully requests the Examiner call the undersigned attorney at the below listed number if the Examiner believes it would be helpful in resolving any remaining issues.

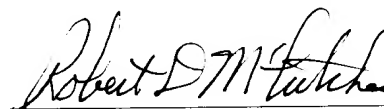
The Commissioner is hereby authorized to charge any additional fees connected with this paper or credit any overpayment to Deposit Account No. 19-1353.

Respectfully submitted,

Attorney for Applicant

Date: Oct. 12, 2001

By:



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